

PCN # 14 0006

QUALIFICATION PLAN				
TEST	SPECIFICATION	PACKAGE	SAMPLE SIZE	EXPECTED COMPLETION DATE
Solder Heat Resistance (SHR)*	JEDEC/IPC <i>J-STD-020</i>	8-MINI_SO	1 x 30	April 2014
Solder Heat Resistance (SHR)*	JEDEC/IPC <i>J-STD-020</i>	8-SOIC_N	1 x 30	April 2014
Latch-Up	JEDEC <i>JESD78</i>	8-SOIC_N	1 x 9	March 2014
Electrostatic Discharge <i>Human Body Model</i>	ESDA/JEDEC <i>JS-001</i>	8-SOIC_N	3/voltage	March 2014
Electrostatic Discharge <i>Machine Model</i>	JEDEC <i>JESD22-A115</i>	8-SOIC_N	3/voltage	March 2014
Electrostatic Discharge <i>Field-Induced Charged Device Model</i>	JEDEC <i>JESD22-C101</i>	8-MINI_SO	3/voltage	March 2014
Electrostatic Discharge <i>Field-Induced Charged Device Model</i>	JEDEC <i>JESD22-C101</i>	8-SOIC_N	3/voltage	March 2014

*Preconditioned per JEDEC/IPC J-STD-020